

Figures

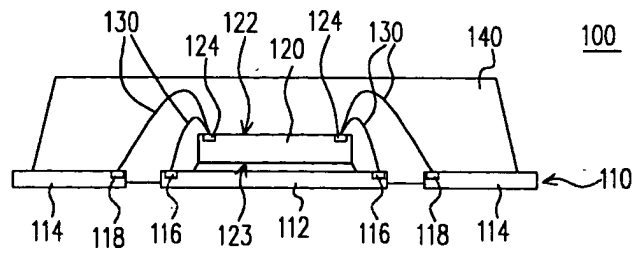


FIG. 1 (PRIOR ART)

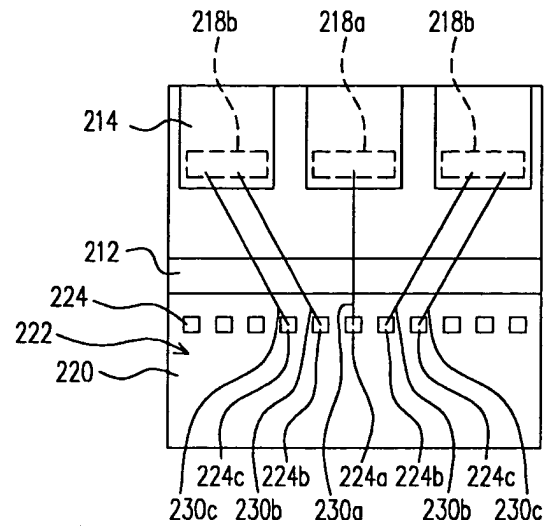


FIG. 2A(PRIOR ART)

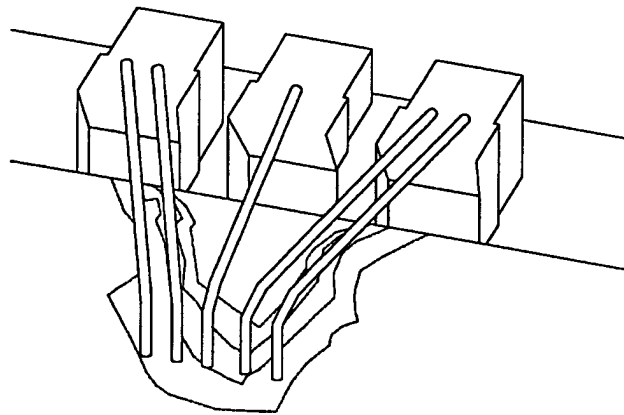


FIG. 3A(PRIOR ART)

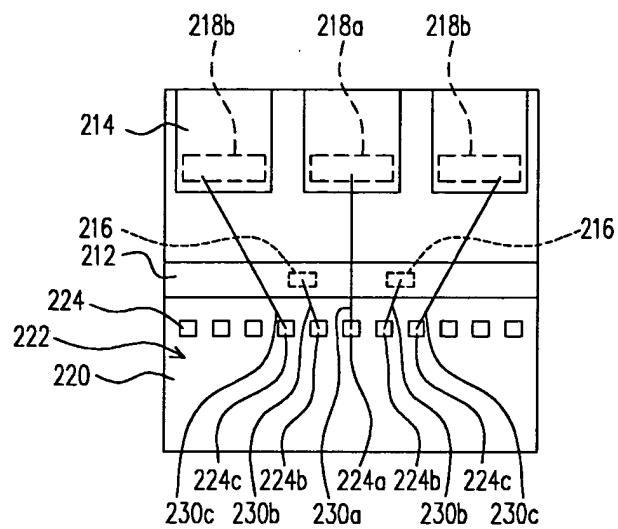


FIG. 2B(PRIOR ART)

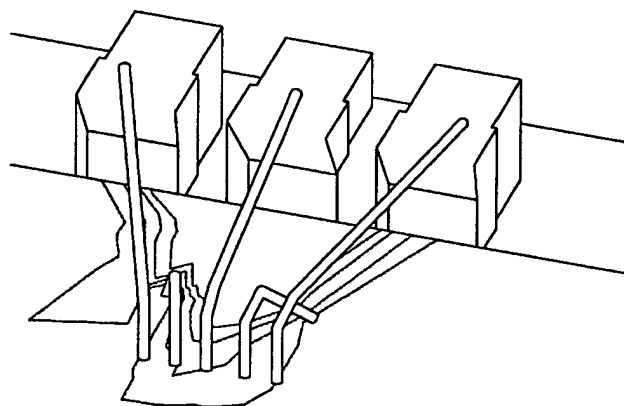


FIG. 3B(PRIOR ART)

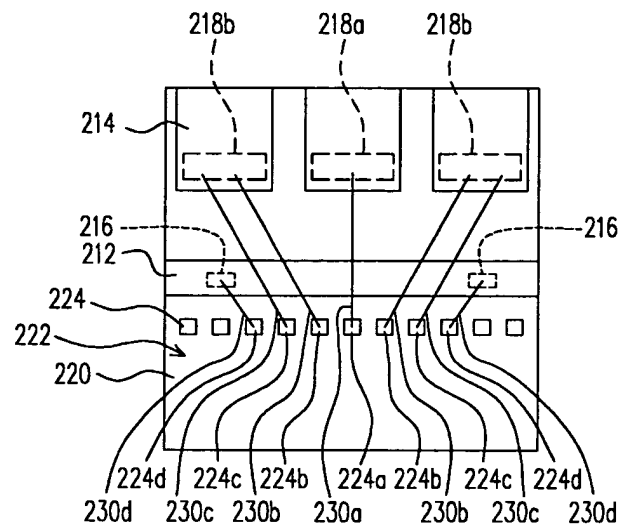


FIG. 2C(PRIOR ART)

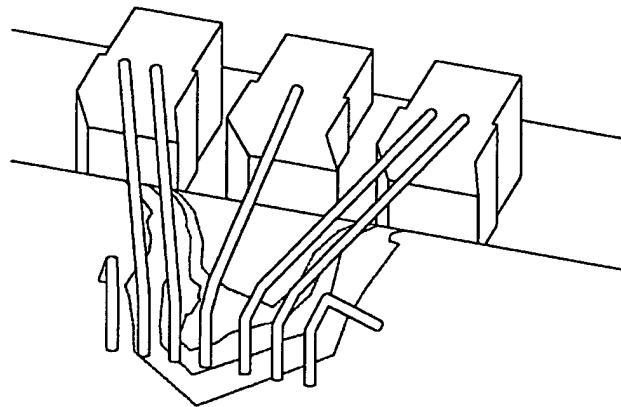


FIG. 3C(PRIOR ART)

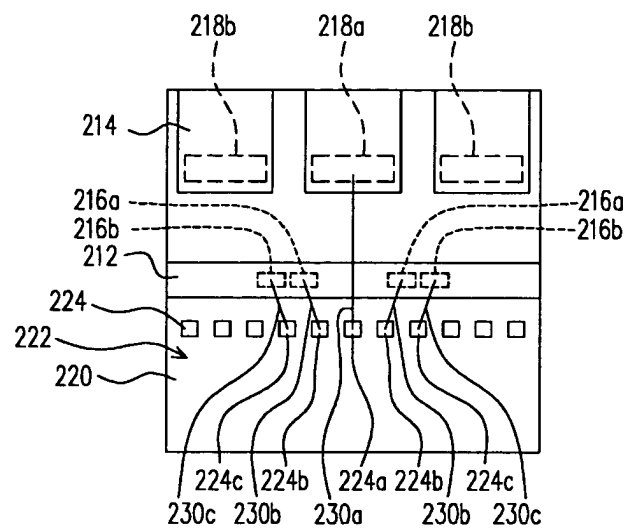


FIG. 2D(PRIOR ART)

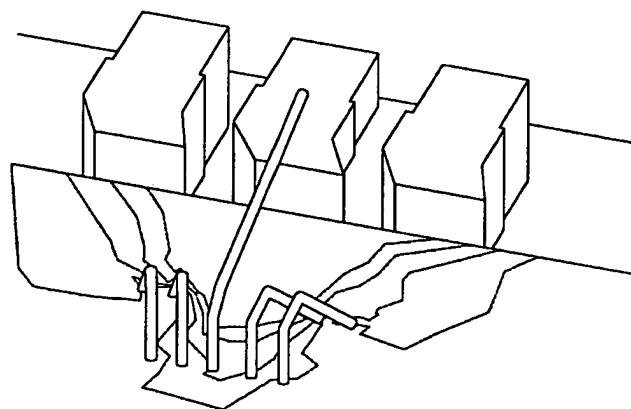


FIG. 3D(PRIOR ART)

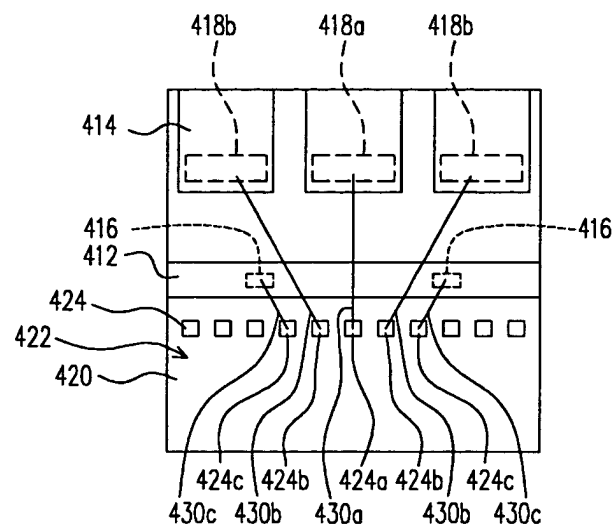


FIG. 4

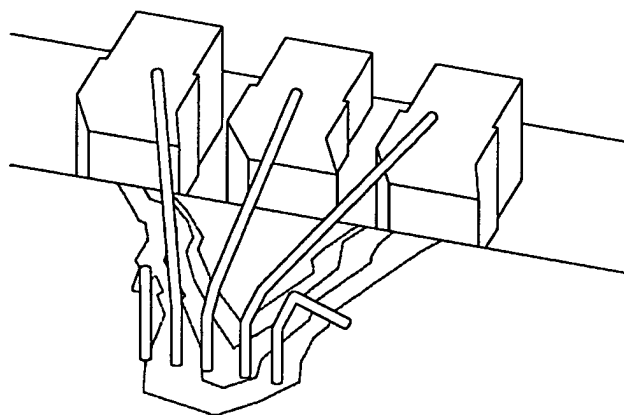


FIG. 5

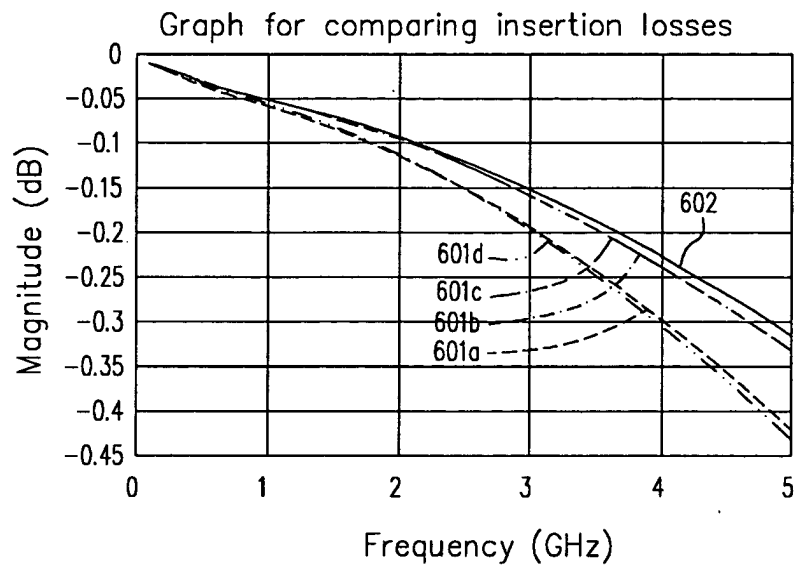


FIG. 6 (PRIOR ART)

Table 1
(PRIOR ART) Table for comparing inserting losses

Insertion loss (dB)	2.4GHz	5GHz
First type of conventional guard circuit design	-0.128	-0.371
Second type of conventional guard circuit design	-0.117	-0.333
Third type of conventional guard circuit design	-0.117	-0.332
Fourth type of conventional guard circuit design	-0.143	-0.432
The guard circuit design according to this invention	-0.114	-0.315

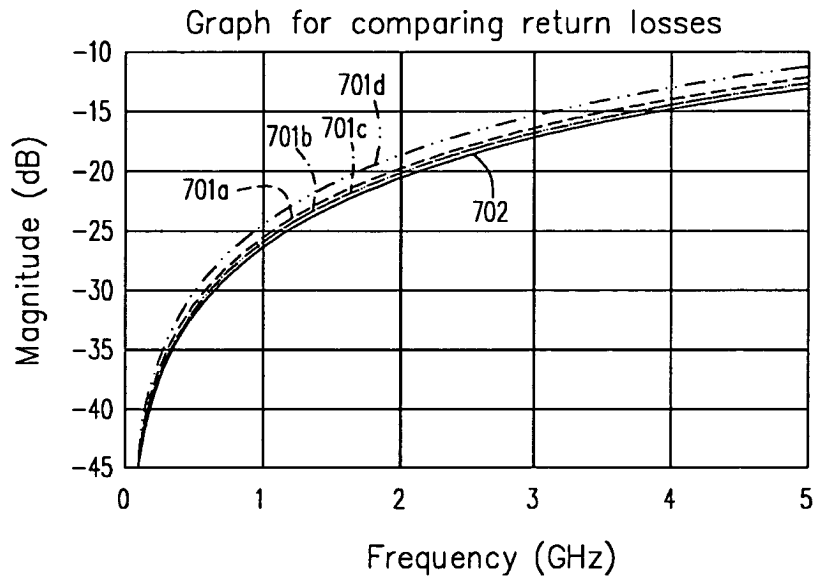


FIG. 7 (PRIOR ART)

Table 2
(PRIOR ART) Table for comparing return losses

Return loss (dB)	2.4GHz	5GHz
First type of conventional guard circuit design	-18.26	-12.22
Second type of conventional guard circuit design	-18.71	-12.73
Third type of conventional guard circuit design	-18.71	-12.73
Fourth type of conventional guard circuit design	-17.17	-11.28
The guard circuit design according to this invention	-19.04	-13.07

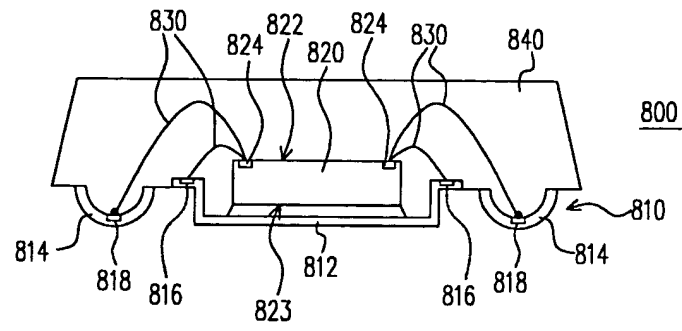


FIG. 8

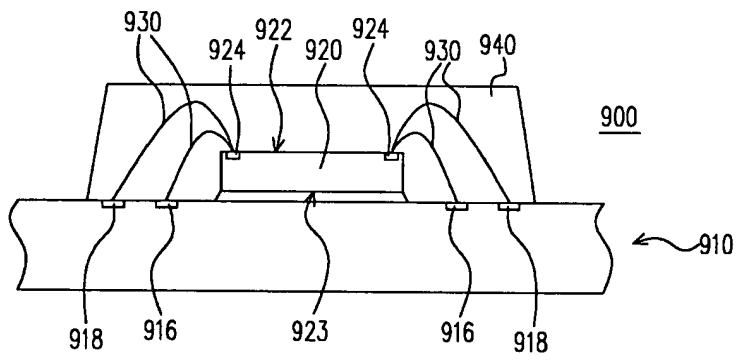


FIG. 9